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PATENT  
450100-03853

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Masaki ENDO  
Serial No. : 10/084,789  
Filed : February 25, 2002  
For : DELAY LOCK LOOP CIRCUIT, VARIABLE DELAY CIRCUIT,  
AND RECORDING SIGNAL COMPENSATING CIRCUIT  
Art Unit : 2816

745 Fifth Avenue  
New York, New York 10151  
Tel. (212) 588-0800

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Assistant Commissioner for Patents  
Washington, D.C. 20231, on April 19, 2002

Gordon Kessler, Reg. No. 38,511

Name of Applicant, Assignee or  
Registered Representative

*Gordon Kessler*  
Signature

April 19, 2002  
Date of Signature

REQUEST FOR CORRECTED FILING RECEIPT

Application Processing Division  
Customer Correction Branch  
Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

This is a Request to correct the Filing Receipt issued in  
the above-identified application.

Under Foreign Applications:

Please insert --JAPAN 2001-163818 05/31/2001--.

The foregoing changes are needed to correct inadvertent  
errors, as evidenced by the Declaration submitted with the patent

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application. A copy of the Filing Receipt with the requested correction(s) noted thereon in red ink is enclosed.

The issuance of a corrected Filing Receipt is respectfully requested.

Please charge any fees required for this correction or credit any overpayment to Deposit Account No. 50-0320.

Respectfully submitted,

FROMMER LAWRENCE & HAUG LLP  
Attorneys for Applicant

By: 

Gordon Kessler

Reg. No. 38,511

Tel. (212) 588-0800

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/084,789	02/25/2002	2816	992	450100-03853	18	12	6

CONFIRMATION NO. 6221

## FILING RECEIPT



\*OC000000007792141\*

WILLIAM S. FROMMER, Esq.  
c/o FROMMER LAWRENCE & HAUG LLP  
745 Fifth Avenue  
New York, NY 10151

Date Mailed: 04/04/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Masaki Endo, Tokyo, JAPAN;

## Domestic Priority data as claimed by applicant

## Foreign Applications

JAPAN P2001-050433 02/26/2001

JAPAN 2001-163818 05/31/2001

If Required, Foreign Filing License Granted 04/03/2002

Projected Publication Date: 08/29/2002

Non-Publication Request: No

Early Publication Request: No

## Title

Delay lock loop circuit, variable delay circuit, and recording signal compensating circuit

## Preliminary Class

327

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